

A2 3. (Amended) The circuit of claim 1, further comprising a sense amp, said sense amp and said differential sense latch coupled such that, in operation, differential signals present on differential output terminals of said sense amp cause an electronic signal to be stored in said differential sense latch. \*

A3 10. (Amended) The circuit of claim 1, further comprising a differential domino circuit, said differential domino circuit and said differential sense latch being coupled such that, in operation, differential output signals present on differential output terminals of said differential domino circuit cause a corresponding electronic signal to be stored in said differential sense latch.

11. (Amended) A method for storing electronic signals produced by a differential circuit comprising:  
pre-charging said differential circuit;  
evaluating said differential circuit;  
sensing differential output signals via a differential sense circuit, wherein said differential sense circuit is coupled to a latch in a push-pull configuration; and  
storing an electronic signal corresponding to said differential output signal.

A4  
Sub B3 17. (Amended) An integrated circuit comprising:  
a plurality of datapaths, at least one of said datapaths comprising:  
a differential circuit and a differential sense latch, wherein said differential sense latch comprises a differential sense circuit and a jam-latch coupled such that, in operation, an electronic signal based, at least in part, on differential output terminals of said differential circuit is stored in said jam-latch, wherein said differential sense circuit is coupled to said jam-latch in a push-pull configuration.